

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS:

Currie et al.

CONFIRMATION NO.:

7878

SERIAL NO.:

10/696,994

GROUP NO.:

2823

FILING DATE:

October 30, 2003

EXAMINER:

Brewster, W.

TITLE:

METHODS FOR PRESERVING STRAINED SEMICONDUCTOR

SUBSTRATE LAYERS DURING CMOS PROCESSING

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL AMENDMENT

Applicants respectfully submit the following Amendment to supplement the Amendment and Response filed along with the Request for Continued Examination on October 25, 2005. Applicants believe that no extra claims fee or other fee is due for this Supplemental Amendment to be entered and considered. However, please consider this a conditional authorization to charge any fees necessary for entry of this paper to Deposit Account No. 07-1700.

Amendments to the Claims begin on page 2

Applicants' Remarks begin on page 7.